

Claims

1. A test probe for a high-frequency device having two or more contact regions, the test probe comprising:
 - at least one signal probe tip having a contact surface area for contacting one of the contact regions of the device; and
 - a ground probe having a ground contact surface with a surface area substantially greater than the contact surface area of the one signal probe tip for contacting another one of the contact regions of the electronic circuit.
2. The test probe of claim 1, wherein the contact surface area of the ground contact surface is at least ten times greater than the contact surface area of the one signal probe tip.
3. The test probe of claim 2, wherein the ground contact surface comprises two or more noncontiguous contact regions.
4. The test probe of claim 2, wherein the ground contact surface consists of a continuous contact surface.
5. The test probe of claim 1, wherein the ground probe has a non-contact surface substantially parallel to the contact surface and spaced from the contact surface of the ground probe to define a nominal characteristic impedance when the contact surface of the ground probe contacts the other one of the contact regions of the device.
6. The test probe of claim 1, further comprising a coaxial connector coupled to the signal probe tip.
7. The test probe of claim 1, wherein the device includes at least one DC contact region for receiving a DC bias input, and wherein the test head further comprises at least one DC bias tip for contacting the one DC contact region and providing a DC bias signal.

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8. The test probe of claim 1 wherein the ground probe is removable to allow its replacement with another ground probe.
9. The test fixture of claim 1, wherein the one signal probe tip has a tip axis which defines a non-right angle with the work surface.
10. A test system for testing two or more microwave devices, with each device having at least one signal port, at least one ground pad, and at least one DC input-output pad, the system comprising:
- a work surface for supporting the two or more microwave devices;
 - a test head including:
 - at least one signal probe tip having a contact surface area for contacting the one signal port of a first one of the microwave devices; and
 - a first ground probe having a ground contact surface with a contact surface area substantially greater than the contact surface area of the one signal probe tip for contacting the one ground pad of the first one of the microwave devices;
 - programmable means for sequentially moving the test head into alignment with each of the microwave devices on the work surface and for bringing the signal probe tip into contact with the one signal port of each microwave device and the contact surface of the first ground probe into contact with the one ground pad of each microwave device.

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11. The test fixture of claim 10:
- wherein the test head further comprises at least one DC probe tip for contacting the one DC-input-output pad of the first one of the microwave devices; and
 - wherein the test fixture further comprises a DC bias circuit coupled to the one DC probe tip.

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12. The test fixture of claim 10 wherein the first ground probe is removable to allow its replacement with a second ground probe having a second ground contact surface with a second contact surface area that differs from that of the first ground probe.

13. The test fixture of claim 10, wherein the one signal probe tip has a tip axis which defines a non-right angle with the work surface.

14. A test probe comprising:
first and second signal probe tips;
a single ground structure between the first and second probe tips.

15. The test probe of claim 14, wherein:
the first and second signal probe tips have respective first and second contact areas; and
wherein the ground structure includes a ground contact area which is greater than at least one of the first and second contact areas.

16. The test probe of claim 14, wherein the ground structure includes a ground contact surface and non-contact surface substantially parallel to the ground contact surface and offset from the contact surface.

17. A method of testing microwave or high-frequency devices, with each device having at least one signal port, at least one ground pad, and at least one DC input-output pad, the method comprising:

providing a test head comprising at least one signal probe tip having a contact surface area, and a first ground probe having a ground contact surface with a ground contact surface area substantially greater than the contact surface area of the one signal probe tip; and

moving the signal probe tip into contact with the one signal port of a first one of the devices and the contact surface of the first ground probe into contact with the one ground pad of the first one of the devices.

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18. The method of claim 17, further comprising:
measuring or analyzing an electrical signal communicated through the signal probe tip after moving the signal probe tip into contact with the one signal port of the first one of the devices.
19. The method of claim 18, further comprising:
determining whether the first one of the devices is acceptable or unacceptable based the measurement or analysis of the electrical signal;
moving the signal probe tip into contact with the one signal port of a second one of the devices and the contact surface of the first ground probe into contact with the one ground pad of the second one of the devices;
measuring or analyzing an electrical signal communicated through the signal probe tip after moving the signal probe tip into contact with the one signal port of the second one of the devices; and
determining whether the second one of the devices is acceptable or unacceptable based on the measurement or analysis of the electrical signal;
20. A method of manufacturing a device comprising a high-frequency electronic assembly and a circuit board, the method comprising:
providing a test head comprising at least one signal probe tip having a contact surface area, and a first ground probe having a ground contact surface with a ground contact surface area substantially greater than the contact surface area of the one signal probe tip;
testing the high-frequency electronic assembly using the test head; and
mounting the high-frequency electronic assembly to the circuit board after testing it.

21. The method of claim 20, wherein testing the high-frequency electronic assembly comprises applying a signal having a nominal frequency greater than 10 gigahertz to the assembly.

22. The method of claim 20, wherein testing the high-frequency electronic assembly comprises applying a signal having a nominal frequency greater than 20 gigahertz to the assembly.

23. The method of claim 20 wherein mounting the high-frequency electronic assembly comprising surface mounting the assembly to the circuit board.

24. The method of claim 20 wherein the circuit board includes one or more signal ports which lack non-adjacent grounds.

25. A method of manufacturing a device comprising a high-frequency electronic assembly having at least first and second conductive regions, the method comprising:

providing a test head having first and second test-head contacts;
establishing electrical communication between the first test-head contact and the first conductive region and between the second test-head contact and the second conductive region;

sensing electrical communication between the first test-head contact and the first-head conductive region;

automatically introducing a test signal through the second test-head contact into the electronic assembly in response to sensing electrical communication between the first test-head contact and second conductive region.

26. The method of claim 25, wherein establishing electrical communication between the first test-head contact and the first conductive region comprises establishing a DC current between the first test-head contact and the first conductive region.

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27. The method of claim 26, wherein automatically introducing a test signal through the second test-head contact into the electronic assembly comprises introducing a signal having a frequency greater than one Giga-Hertz.

28. The method of claim 26, wherein automatically introducing a test signal through the second test-head contact into the electronic assembly comprises introducing the test signal a predetermined time period after sensing electrical communication between the first test-head contact and the first-head conductive region.

29. For a surface-mount package having first and second signal ports and a ground pad between the signal ports, a method of establishing an electrical coupling of a predetermined nominal characteristic impedance with at least the first signal ports, the method comprising:

contacting the first signal port with a first electrical conductor; and

contacting the ground pad with a ground probe, with the ground probe having a surface overhanging a major surface of the signal port and being substantially parallel to the major surface.

30. The method of claim 29, wherein the surface of the ground probe overhanging the major surface of the signal port establishes an electrical field perpendicular to the surface of the ground pad and the major surface of the signal port.

31. A method comprising:

providing a test head having first and second conductive probes for contacting respective conductive portions of a first electronic assembly and establishing a first nominal characteristic impedance; and

changing at least one of the first and second conductive probes to enable the test head to establish a second nominal characteristic impedance different from the first nominal characteristic impedance or to establish the first nominal characteristic

impedance with a different arrangement of conductive portions on a second electronic assembly.

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32. The method of claim 32, wherein changing at least one of the first conductive probes comprises replacing the first conductive probe with a different conductive probe.

33. The method of claim 32, wherein the first conductive probe is a ground probe.